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Applicant : Hideomi Suzawa et al.

Art Unit : 2812

Serial No. : 09/852,672

Examiner : Viktor Simkovic

Filed : May 11, 2001

Confirmation No.: 5083

Title : SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF

SEP 17 2004

OFFICE OF PETITIONS

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INFORMATION DISCLOSURE STATEMENT

Copies of the references listed on the attached form PTO-1449 are enclosed.

This filing is being made with the filing of a Request for Continued Examination. No fee is required.

Respectfully submitted,


John F. Hayden
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Date: September 17, 2004

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Substitute Form PTO-1449 (Modified)		U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 12732-038001	Application No. 09/852,672
Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))		Applicant Hideomi Suzawa et al.	RECEIVED SEP 17 2004 2812 OFFICE OF PETITIONS	
		Filing Date May 11, 2001	Group Art Unit 2812	

U.S. Patent Documents

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	2002/0134983 A1	09/26/2002	Yamazaki			02/22/2002
	AB	2003/0207503 A1	11/06/2003	Yamazaki et al.			05/02/2003
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	AK						

Foreign Patent Documents or Published Foreign Patent Applications

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	AL							
	AM							
	AN							
	AO							
	AP							

Other Documents (include Author, Title, Date, and Place of Publication)

Examiner Initial	Desig. ID	Document
	AQ	
	AR	
	AS	
	AT	

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EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	